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Date Created	Type	ICS
Mar 4, 2016	Main	ICS

Design Name EYSHCN**Design Model Number** EYSHCN**Hardware Version Number** 1.0**Software Version Number** N/A**Qualification Assessment Date** March/15/2016**Listing Date** March/15/2016

Design Description Single Mode Bluetooth Smart Module. Integrated system/sleep clock and high performance antenna. Interface: SPI, UART, I2C and 12bit ADC. Single power supply: 1.7 - 3.6V. ARM Cortex-M4F 32bit processor. 256/512kB Flash and 32/64kB RAM. Japan, FCC and IC qualified. Outline: 9.6×12.9×2.0 mm. RoHS compliant / Complete Lead Free.

Product Type Component (Tested)**Specification Name** 4.2

Product List

Affiliate	Product Name	Model Number	URL	Description	Subset ID	Publish Date
Taiyo Yuden Co., Ltd		EYSHCNZXZ	http://www.yuden.co.jp/ut/solutions/ble/	Single Mode Bluetooth Smart Module. Integrated system/sleep clock and high performance antenna. Interface: SPI, UART, I2C and 12bit ADC. Single power supply: 1.7 - 3.6V. ARM Cortex-M4F 32bit processor. 512kB Flash and 64kB RAM. Japan, FCC and IC qualified. Outline: 9.6×12.9×2.0 mm. RoHS compliant / Complete Lead Free.		Mar 15, 2016

Listed By [Hideki Kato](#)**BQE** [Shoichi Kamata](#)

Referenced QDID Profile Information

[QDID: 81272](#)